

**PATENT** 

Attorney Docket No.: SAM-0126 ECHALOGY SALE

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: Nguyen, C.

Group Art Unit: 2811

Applicant(s): In-Jung Lee, et al.

Serial No.:

09/633,366

Filing Date: Title:

August 7, 2000

CAPACITOR AND FABRICATING METHOD THEREFOR

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to BOX AF, Assistant Commissioner for Patents, Washington, DC 20231.

**BOX AF** 

**Assistant Commissioner for Patents** 

Washington, D.C. 20231

**AMENDMENT AFTER FINAL REJECTION** 

Sir:

This is in response to the final Office Action mailed on August 26, 2002. It is requested that the following amendments be entered and that the following remarks be considered.

Please amend the application as follows:

## In the Claims

Please cancel claim 4.

Please amend the following claims:

1.

(Twice Amended) A semiconductor capacitor having a lower electrode, a dielectric layer and an upper electrode, wherein the upper electrode comprises a deposition structure including three layers including a doped polysilicon layer formed between a first undoped